



**REPLY UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2123**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/008,270  
Filed: November 9, 2001  
Inventor(s):  
Carl Cavanaugh, Carl B. Frankel,  
James P. Freyensee, and Steven  
A. Sivier

Title: Verification Simulator  
Agnosticity

Examiner: Proctor, Jason Scott  
Group/Art Unit: 2123  
Atty. Dkt. No: 5181-96200

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Lawrence J. Merkel

Printed Name                     

Signature \_\_\_\_\_

Date \_\_\_\_\_

9/2/05

**RESPONSE TO OFFICE ACTION OF  
July 6, 2005**

**ATTN: Mailstop AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of July 6, 2005, to further highlight why the application is in condition for allowance.